



Diodato 9-7-17-2

*IDW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Diodato et al.  
Case: 9-7-17-2  
Serial No.: 09/865,847  
Filing Date: May 25, 2001  
Group: 2115  
Examiner: Chun Cao

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *Kevin Mason* Date: September 14, 2005

Title: Apparatus for Reducing Leakage Power in a Cache Memory by Using Timer Control Signal Removes Power to Associated Cache Lines

AMENDMENT TRANSMITTAL

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith is an Amendment After Allowance Under 37 CFR §1.312 relating to the above-identified patent application. There is no additional claim fee due in connection with the Amendment.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

*Kevin M. Mason*

Kevin M. Mason  
Attorney for Applicants  
Reg. No. 36,597  
Ryan, Mason & Lewis, LLP  
1300 Post Road, Suite 205  
Fairfield, CT 06824  
(203) 255-6560

Date: September 14, 2005



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## Patent Application

Applicant(s): Diodato et al.

Docket No.: 9-7-17-2

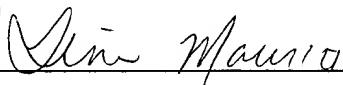
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AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312

20 Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

25 Sir:

A Notice of Allowance and Issue Fee Due, mailed July 11, 2005 has been received in connection with the above-identified patent application. Please amend the above-identified allowed 30 patent application as indicated on the attached sheets.

IN THE TITLE:

Please amend the title as indicated below:

Method and Apparatus for Reducing Leakage Power in a Cache Memory by Using a  
Timer Control Signal that Removes Power to Associated Cache Lines